

dc Power System Design for Telecommunications

by Whitham D. Reeve

Errata Sheet ©2010 Whitham D. Reeve

Pg 81, Eq. 2.54 should read $P_{3-\phi} = \sqrt{3}V_{L-L}I_LPF$

Pg 239, Table 5.5(b) has two incorrect entries as follows:

Minimum VRLA float voltage (2.27 V/cell) for 48-V System should be 54.48 V

Minimum VRLA float voltage (2.23 V/cell) for 48-V System should be 53.5 V